

EMC Design in the IC Environment with respect to ESD and Burst

Short description

Today's integrated circuits (IC) are very sensitive to disturbances. Fast pulses which were not perceived by slower ICs in the past may now lead to serious disturbances or even total failure. The characteristics of ICs can no longer be ignored if one wishes to ensure a high immunity to electromagnetic disturbances of electronic devices. Burst and ESD disturbances enter electronic devices from outside and reach the pins of ICs via conductors. Disturbances enter the ICs both through their pins and also directly via magnetic and electrical fields.