

P202 L-EFT

Langer EFT 电流发生器 (~40V) SMA



Short description

P201 L-EFT型电流发生器用于耦合受测集成电路导线中的脉冲电流。与IEC 61000-4-2/IEC61000-4-4标准检测相仿，它再现了IC输入端出现的衰减的干扰电流脉冲。在检测时，这些干扰电流脉冲是由磁场生成的。

When a magnetic disturbance field couples into a low impedance loop connected to a pin of the test IC, a disturbance voltage will be induced. This voltage drives a disturbance current pulse into the IC pin and can lead to functional interferences.

This coupling mechanism can be simulated by a low-impedance pulse-current source, such as the P202 generator ($\approx 1 \Omega$). The pulse voltage is adjustable within the range of $\pm (0.5 - 40) \text{ V}$. The L-EFT generator is operated with the BPS 202 and the BPS 202-Client software. To test ICs at the measuring station the ICE1 test environment is required. For automated testing the ICE1 as well as the ICT1 IC tester is required.

Additional equipment may be needed for other measurements (Oscilloscope, PC).

Technical parameters

内阻	$\approx 1 \Omega$
耦合电容的容量	$1.2 \mu\text{F}$
脉冲参数	
形状	1.5 / 5 ns
频率	0.1 Hz - 10 kHz
电压	$\pm (0.4 - 40) \text{ V}$
电感	$\approx 2 \text{ nH}$
尺寸 (L x W x H)	(78 x 35 x 31) mm